

August 2019 Volume 7 | ISSUE 2

V.V. Sangha's

Rao Bahadur Y. Mahabaleswarappa Engineering College, Ballari.  
Department of Electronics & Communication Engineering

## TALENTRONIC TIMES



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E&CE Department



Releasing Abstract contents of selected papers of 1st International Conference on Emerging Trends in Engineering, Science and Management(ICETESM-2019) on 4/04/2019.

## ABOUT THE INSTITUTION

Rao Bahadur Y Mahabaleswarappa Engineering College was established in 1980, under the aegis of the Veerasaiva Vidyavardhaka Sangha, Ballari.

The Institute is affiliated to VTU, Belagavi approved by AICTE, New Delhi. The institute offers 8 UG, 7 PG programs and 6 Research centres. The institute has excellent sports facilities, well equipped labs and digital class rooms & in addition it has separate hostel facilities for boys & girls. The Institute is located 50 Kms away from the world famous historical place "Hampi".

## LAURELS OF E&CE DEPARTMENT

The Department of Electronics and Communication Engineering is established in the year 1983 with an intake of 40. Presently the intake is 120 with a PG course in Digital Communication & Networking and Research Centre. The department was accredited twice by NBA, New Delhi.

The department is well equipped with state-of-the-art laboratories and research facilities, organizing various Workshops, Seminars/Symposiums & Placement Training's. The Department has procured Rs.50 Lakhs under MODROBS scheme AICTE, NEW DELHI, Rs 20 lakhs Grant from VGST scheme of KFIST Level(1) & KSCST Projects sponsorship of Rs.75,000.

Twelve students of the department have bagged Gulbarga University Ranks & three VTU State Ranks. Two Faculties are awarded with State level VTU Ranks for Post Graduate Studies.

### Words of Wisdom

When you discover your mission, you will feel its demand. It will fill you with enthusiasm and a burning desire to get to work on it.

--W. Clement Stone

We always attract into our lives whatever we think about most, believe in most strongly, expect on the deepest level, and imagine most vividly.

--Shakti Gawain

Always do your best. What you plant now, you will harvest later.

--Og Mandino

### Words of Wisdom

Few people attain great lives, in large part because it is just so easy to settle for a good life.

-- Jim Collins

If it is impossible to attain perfection here and now, there is no proof that we can attain perfection in any other life.

--Swami Vivekananda

### QUOTE OF THE ISSUE

If you are humble nothing will touch you, neither praise nor disgrace, because you know what you are.

--Mother Teresa



## EVENTS ORGANIZED

Sl. No	Event Details
1	Two days FDP & Hands on Session on “ <b>Cadence Software</b> ” was organized on February 14 <sup>th</sup> & 15 <sup>th</sup> 2019.
2	Department Forum “ <b>Talentrionics</b> ” was organized in last week of February & main event was on March 2 <sup>nd</sup> 2019.
3	1 <sup>st</sup> International Conference on Emerging Trends in Engineering, Science and Management ( <b>ICETESM-2019</b> ) was organized on 4 <sup>th</sup> & 5 <sup>th</sup> April 2019.
4	One day orientation program on “ <b>IOT</b> ” in association with <b>CISCO-RVCE-COE</b> was organized on June 7 <sup>th</sup> 2019.
5	VEC Reunion at NATS Convention Dallas, TX, USA from May 24 <sup>th</sup> to 26 <sup>th</sup> 2019.
6	Farewell for Final year students was organized on 17 <sup>th</sup> June 2019.



One day orientation program on IOT CISCO-RVCE-COE was organized on June 7th 2019.

## NEWS ABOUT STUDENT PERFORMANCE

### TOPPERS OF DEPARTMENT

Position	4 <sup>th</sup> Sem	SGPA	6 <sup>th</sup> Sem	SGPA	8 <sup>th</sup> Sem	SGPA
1	CHETANA G	9.29	VANDANA K	8.85	DILIP A	9.30
2	SOUNDARYA V S	9.25	TAHREEMNAAZ	8.69	ESTHER RANI	9.15
3	NAGARATHNA	9.32	RAJARAJESHWARI	8.58	HARSHITA K K SAI SHILPA	9.05

### PLACEMENT DETAILS

Our students are placed in many reputed companies. The placement details are as given below:

SL.NO	COMPANY NAME	STUDENTS PLACED
1.	VEE TECHNOLOGIES	7
2.	INFOSYS	4
3.	TCS	2
4.	WIPRO	1
5.	COGNIZANT	2
6.	ELINTZLAB	9

### ACHIEVEMENTS MADE BY STUDENTS IN ACADEMICS

- Ms.Padmashree & team got KSCST funding of Rs 7,000 for project titled **“Dynamic Traffic Light Controller System for Ambulance based on IOT”** under the guidance of Dr.S.Prabhavathi.
- Mr.Jabiwulla S & team got KSCST funding of Rs 7,000 for project titled **“LPG GAS LEAKAGE DETECTION AND AUTOMATIC BOOKING USING ARDUINO UNO”** under the guidance of Mr.Nagraj Gowda.
- Mr.Vinay Kumar.B & batch Secured 1<sup>st</sup> place in **ICETESM-2019** for the paper titled **“Implementation of drowsiness, vehicle safety and alcohol intoxication detection using raspberry pi”** held at RYMEC on 4<sup>th</sup> & 5<sup>th</sup> April 2019.
- Ms.Mushahira & Ms. Tatti Pragnya Secured 2<sup>nd</sup> place in State Level Technical Project Exhibition **INNOVISION 2019** for the project titled **“Gravity Light”** held at PDIT, Hosapete on 3<sup>rd</sup> April 2019.
- Ms.Mushahira & Ms.T.M.Aishwarya had secured first place in Project Exhibition in **“3<sup>rd</sup> State Level Project Exhibition & Code-athon”** organized by department of Computer Science & Engineering, RYMEC on 12<sup>th</sup> & 13<sup>th</sup> April 2019.

## FACULTY CONTRIBUTIONS

### PUBLICATION OF PAPERS (INTERNATIONAL)

1. Mr. Sudharshan Banakar has published paper titled “**Most recent document title: PID controller for thermal control in satellites**” in UGC approved international Journal JETIR June 2019
2. Dr. Savita Sonoli has published paper titled “**Methods Performance Research on Iterative for Image Deblurring**” in UGC approved international Journal JETIR June 2019.
3. Dr. Savita Sonoli has published paper titled “**Design of Power Efficient Differential Amplifier Using 1 UM and 180 NM Technology**” in UGC approved international Journal JETIR June 2019.
4. Dr.S. Prabhavthi & Mr. Channaveerana Gouda has published paper titled “**Design and Comparative Analysis Of algorithm of Processing time, data Aggregation Time, Packet Delivery Radio and Energy**” in UGC approved international Journal JETIR June 2019.

### WORKSHOPS PARTICIPATION

Mrs.Chinna V Gowdar, Mr.Lokesh K S & Mrs.Anitha A had attended 3 days FDP on “**VLSI Physical Design & Verification**” from 25<sup>th</sup> to 27<sup>th</sup> July 2019 at KLEIT Hubli.

### RESEARCH WORK

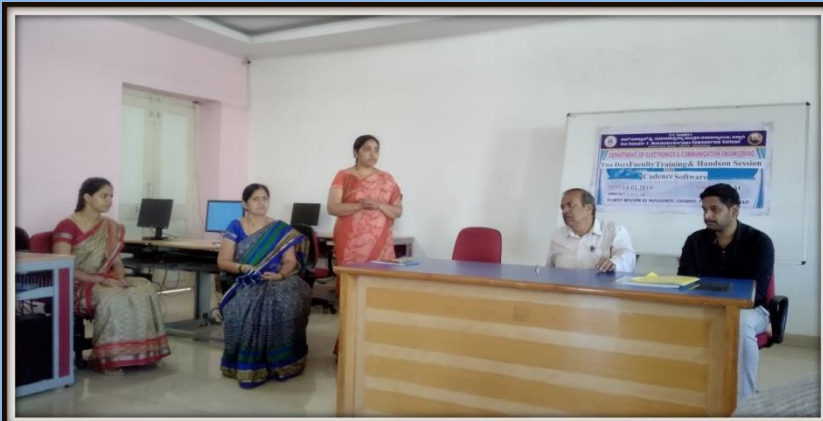
1. Department has been sanctioned **Rs 20 lakhs Grant from VGST scheme of KFIST Level(1)** for the project titled “**Establishment of lab for VLSI design with current technology**”.
2. Dr. Savita Sonoli is guiding Ph.D Research scholars Ms.Chayalaxmi.C.L, Mr.Naveen I G, Mr.G Shashibhushan, Mr.Mahindra.B.M & Mrs.Chinna.V.Goudar registered under VTU, Belagavi.
3. Mr.Surendranath.H, Mrs.Suvarna Patil, Mr.Phanindra Reddy, Mr. Manjunath.K.M Mr.Prashanth Keni & Ms. Manasa.K.C are registered for Ph.D under VTU, Belagavi.

### MOU

The Department has MOU with Tech Trunk Ventures, Hyderabad, Embiot Technologies(ET), Bengaluru, VTU-Friends Union for Engineering Lives(FUEL), Institute for Engineering Research and Publication (IFERP), Chennai. CoreEL Technologies India Pvt. Ltd., Bengaluru, Advanced Electronic Systems (ALS), Bengaluru, Pragna Microdesigns, Bengaluru, BSNL Mysore and CoStuCo Hyderabad.



## PHOTO GALLERY



Two days FDP on Cadence Software on 14th & 15th February 2019.



Felicitation Program on occasion of 1st International Conference on Emerging Trends in Engineering, Science and Management (ICETESM-2019) was organized on 4th & 5th April 2019.



Lagori Event organized on the occasion of Department forum Talentronics.



Lighting of Lamp on Occasion of inauguration of Department Forum Talentronics on 2nd March 2019.



Releasing Department Newsletter "Talentronics Times" on 1st March 2019.



## EVENTS PHOTO GALLERY



VEC Reunion at NATS Convention Dallas, TX, USA from May 24th to 26th 2019.



Faculties Felicitated at VEC Reunion at NATS Convention Dallas, TX, USA.



One day orientation program on IOT CISCO-RVCE-COE was organized on June 7th 2019.



Farewell for Final year students was organized on 17th June 2019.

## VISION, MISSION, PO'S, PSO'S & PEO'S

### VISION OF E&CE DEPARTMENT

To Produce Professionally Excellent, Knowledgeable, Globally Competitive, Socially Responsible Electronics & Communication Engineers and Entrepreneurs.

### MISSION OF E&CE DEPARTMENT

M1: To provide quality education in Electronics & Communication Engineering.

M2: To establish a Continuous Industry-Institute Interaction, Participation and Collaboration to Contribute Skilled Electronics and Communication Engineers.

M3: To develop Human Values, Social Values, Entrepreneurship Skills and Professional Ethics among the Technocrats.

M4: To focus on Innovation and Development of Technologies by Engaging in Electronics and Communication Research areas.

### PROGRAM OUTCOMES (PO'S)

**Engineering Graduates will be able to:**

1. Engineering knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2. Problem analysis: Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. Design/development of solutions: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. Conduct investigations of complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.
6. The engineer and society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. Environment and sustainability: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. Individual and team work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
10. Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. Project management and finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. Life-long learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

### PROGRAM SPECIFIC OUTCOMES (PSO'S)

1. Ability to Design, Develop and Test the Electronics circuits & Communication Systems.
2. Ability to develop excellent programming and problem solving skills in the field of embedded system.

### PROGRAM EDUCATIONAL OBJECTIVE'S (PEO'S)

1. Graduates of Electronics & Communication Engineering course will have Successful Professional Career.
2. Graduates of Electronics & Communication Engineering course will pursue Higher Education or to become an Entrepreneur.
3. Graduates of Electronics & Communication Engineering course will have ability for Lifelong Learning and to serve the Society.